

ABSTRACT

Disclosed is a programmable logic circuit control apparatus and method, and a program, which facilitate smooth execution of multiple processes in complicated procedures. A module address memory section (3) stores data indicating the address of a module to be read by a circuit control section (4) or a condition for a branch process and a jump distance for each page. The circuit control section (4) reads data stored on the topmost page in the module address memory section (3). According to the data, the circuit control section (4) performs reading of a module, reconfiguration of a programmable logic circuit (1) and reading of data of the next page, or jumps. Every time a new page is read, the circuit control section (4) performs processes according to data stored on that page, thereby sequentially reconfiguring the programmable logic circuit (1).